

Figure 1 (Prior Art)

Figure 1A (Prior Art)

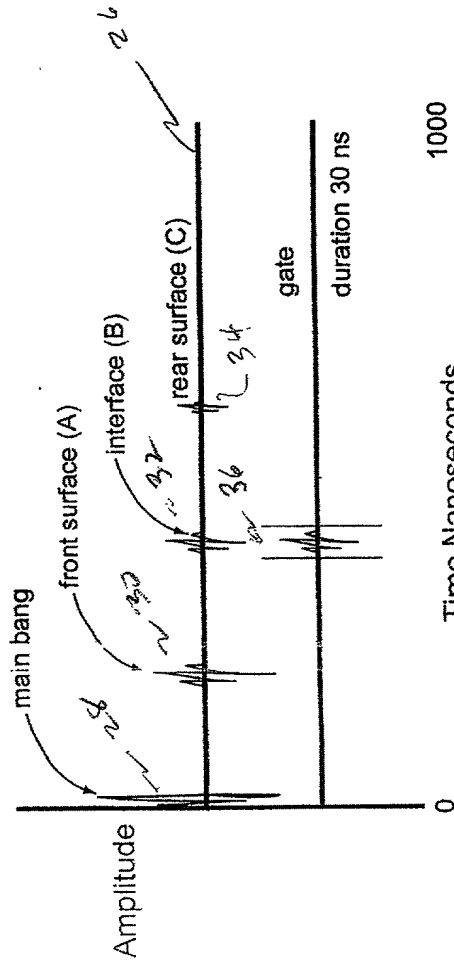


Figure 1B (Prior Art)

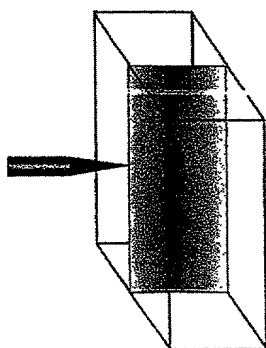


Figure 3
(Prior Art)

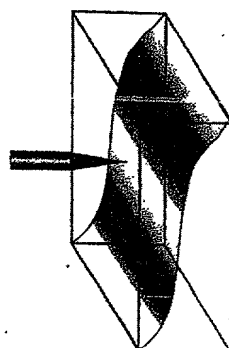


Figure 5
(Prior Art)

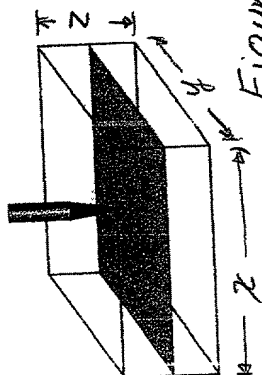


Figure 2
(Prior Art)

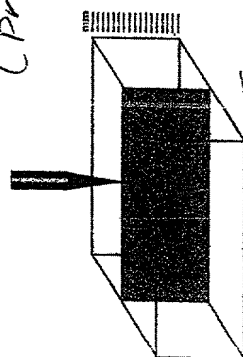


Figure 4 (Prior Art)

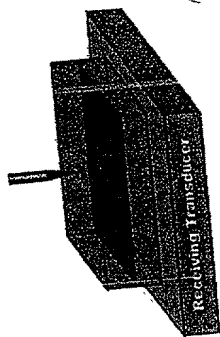


Figure 7
(Prior Art)

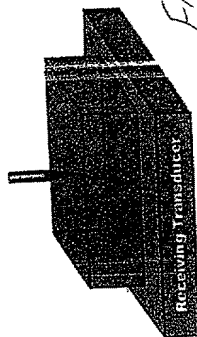


Figure 6
(Prior Art)

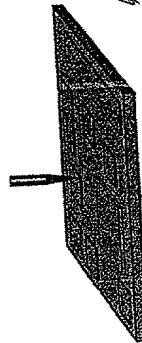


Figure 9
(Prior Art)



Figure 8
(Prior Art)

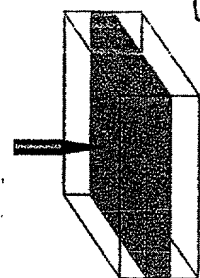


Figure 10
(Prior Art)

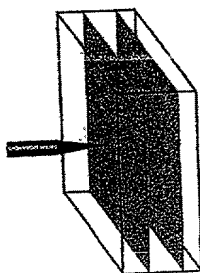


Figure 11
(Prior Art)

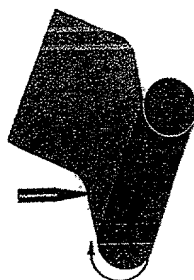


Figure 12
(Prior Art)

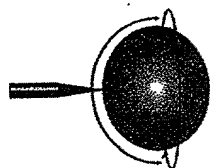


Figure 13
(Prior Art)

Date Ttu Jan 25 2001
 TOF 20.50
 FE Gain/dB 22.500
 Sflce Gain/dB 26.000
 Gate Pos/usec 0.735
 Gate Width/usec 0.554
 Xducer Freq/MHz 5.00
 Xducer FL* 0.774

11(400ns/div) banded lead
 2(400ns/div) disbonded lead
 3(400ns/div) disbonded post

76
 14 84 66
 85
 87 80 68
 86
 89 88

Figure 14 (Prior Art)

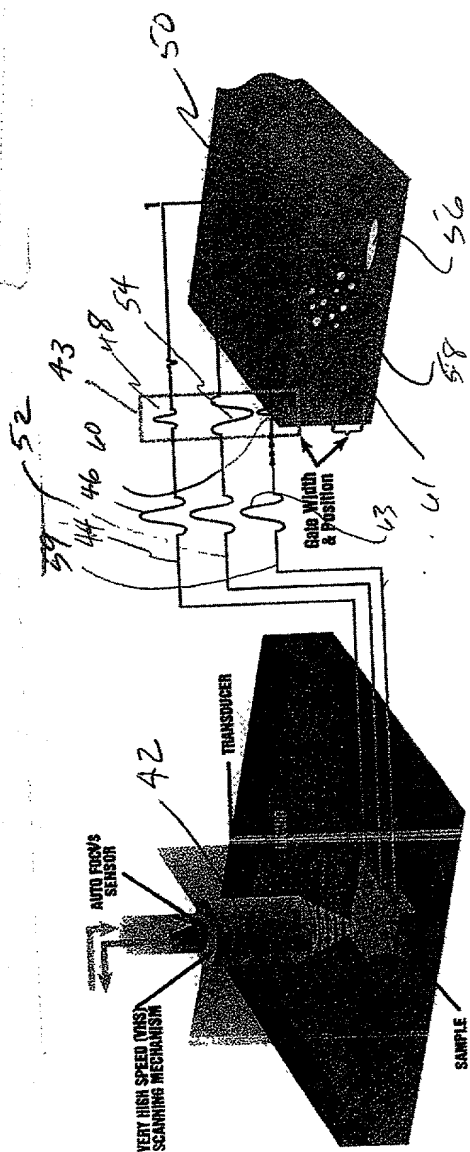


Figure 2A (Prior fit)

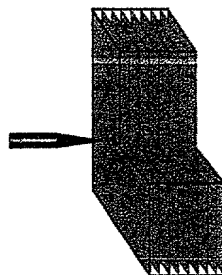


Figure 15
(Prior Art)

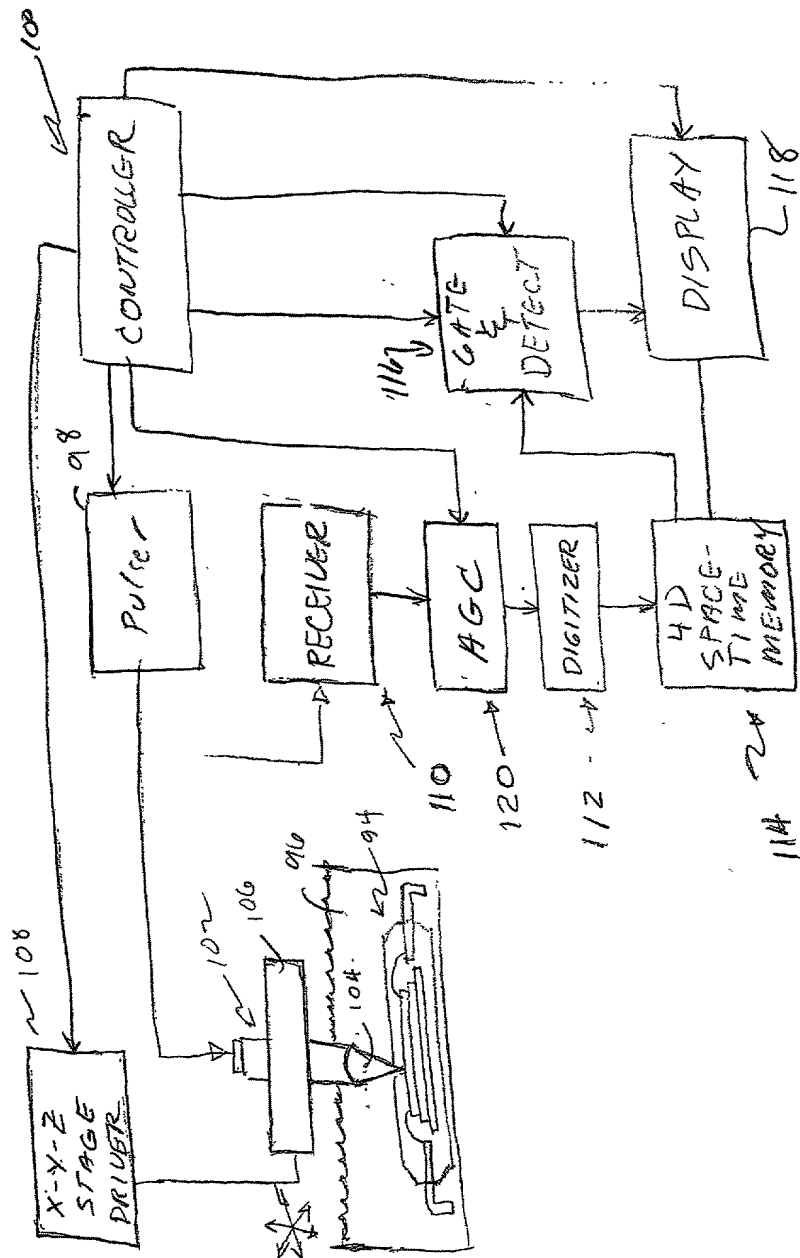


Figure 16

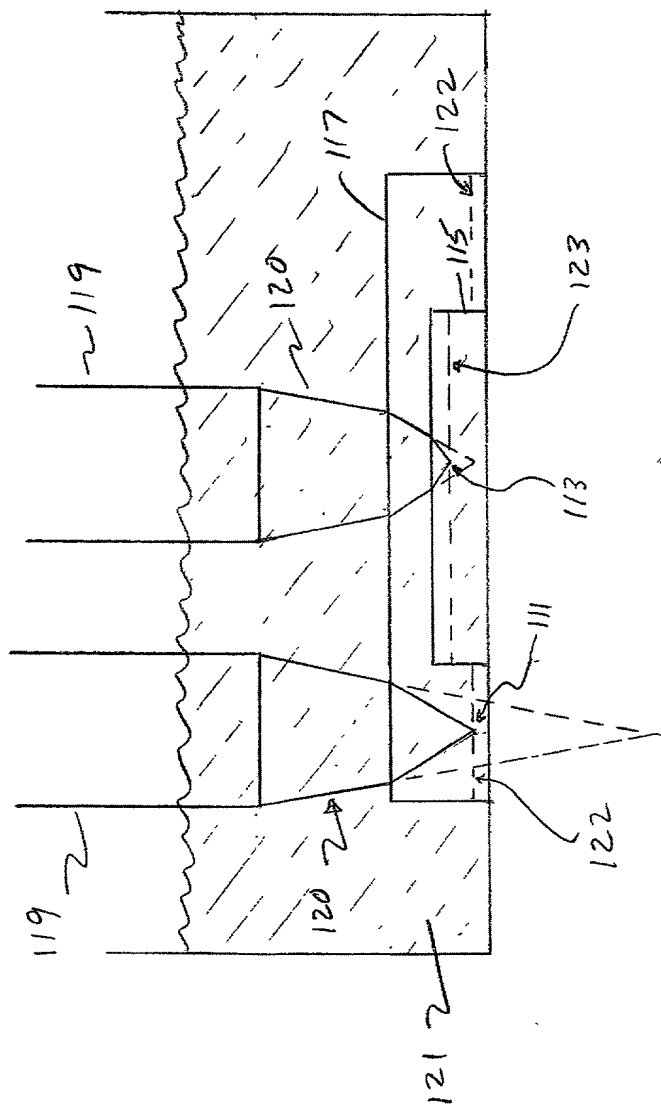


Figure 16A

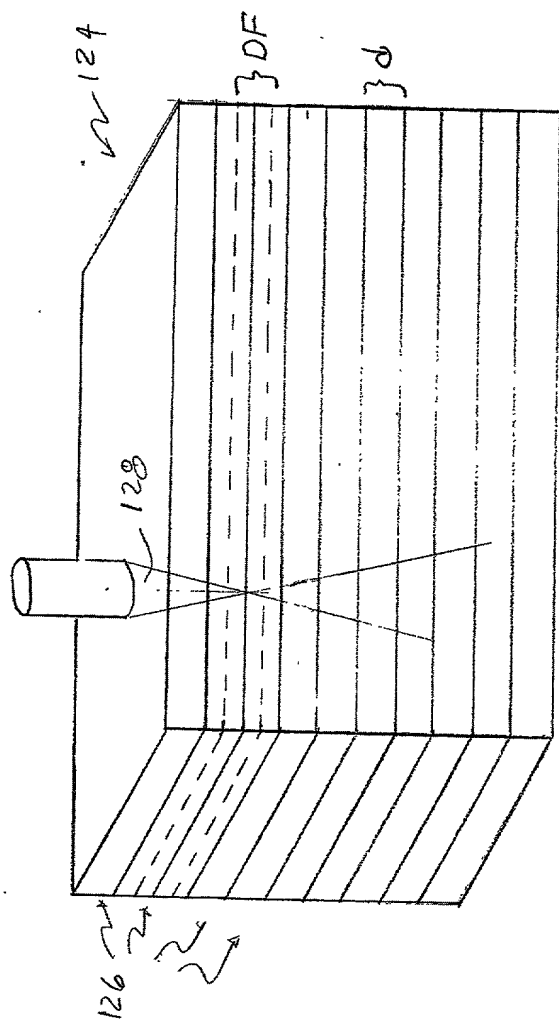


Figure 17

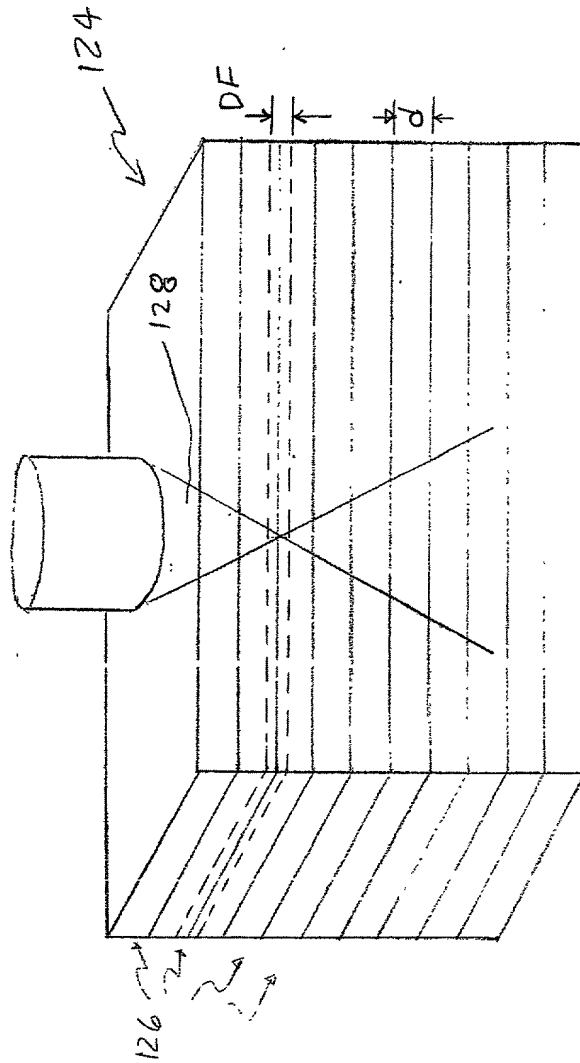


Figure 18

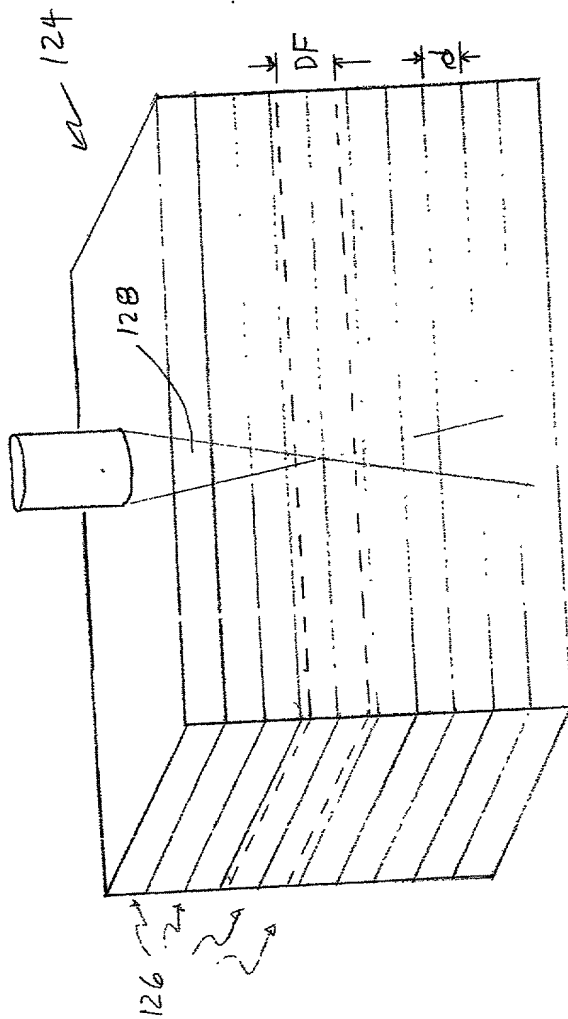


Figure 19

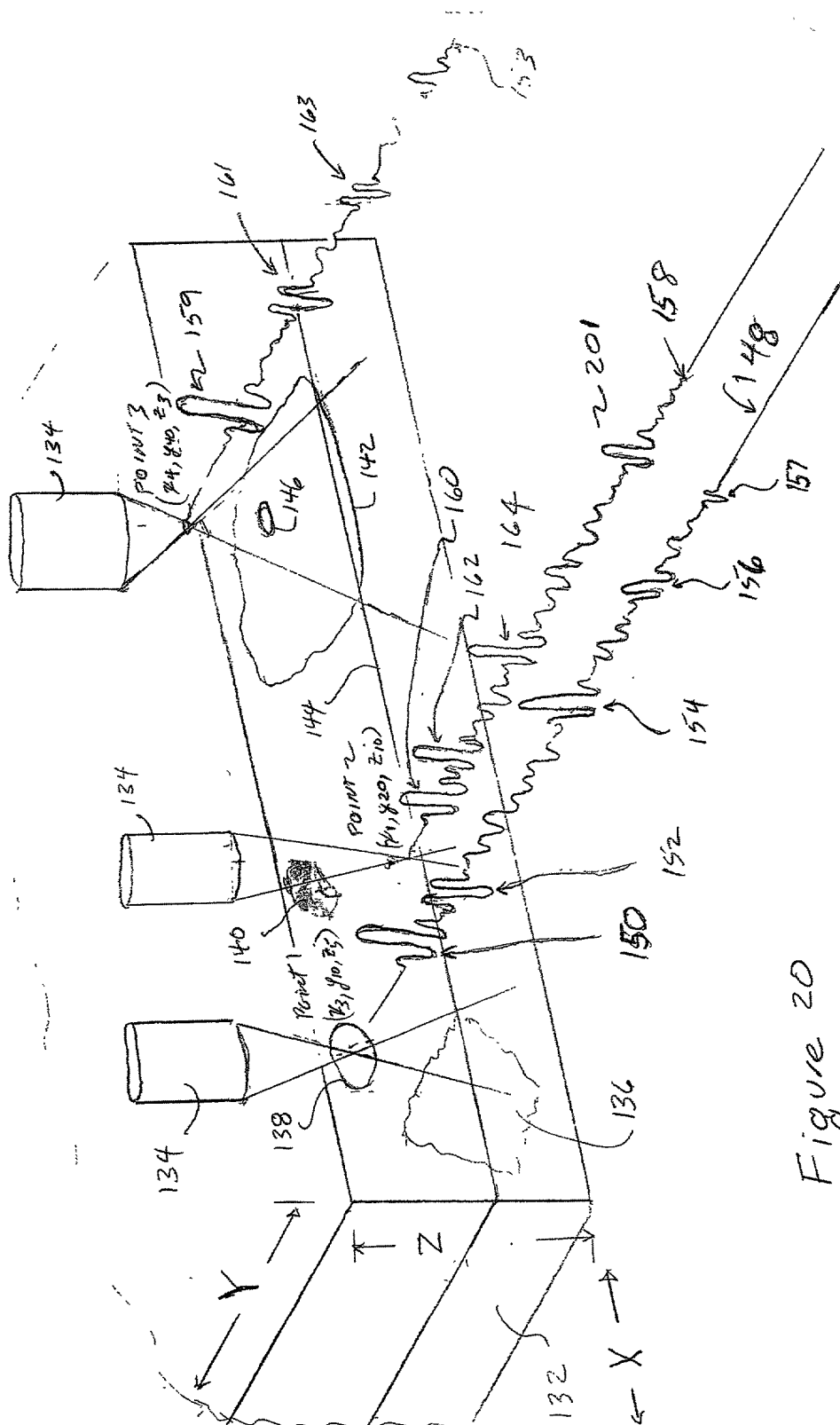


Figure 20

CONFIDENTIAL

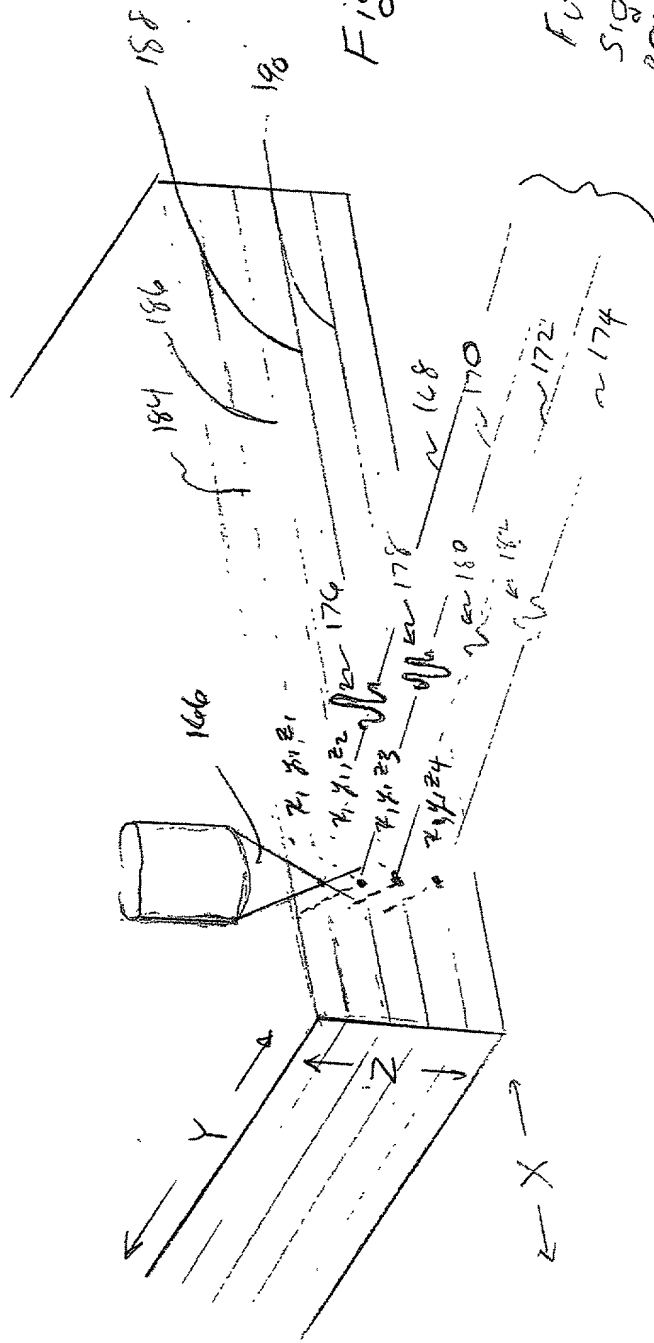


Figure 21

Full echo
signal for
each point digitized
& stored in 4D
memory

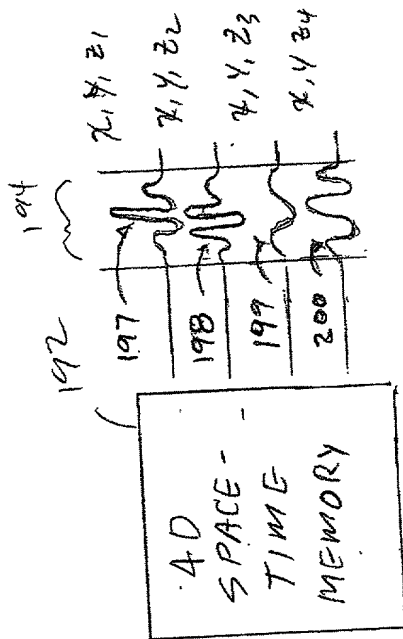


Figure 22

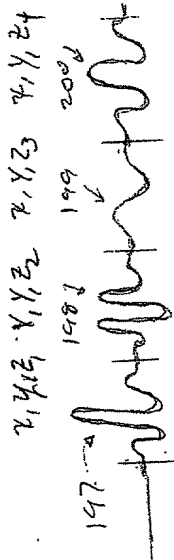


Figure 23